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Application ID:

10785160

Title of Invention:

TWIN EEPROM MEMORY TRANSISTORS WITH SUBSURFACE STEPPED FLOATING

GATES

First Named Inventor:

Bohumil Lojek

Domestic/Foreign Application:

Domestic Application

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ATM-303

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TWIN EEPROM MEMORY TRANSISTORS WITH SUBSURFACE STEPPED FLOATING GATES

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Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney	

Documents being submitted

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us-ids

Files

ATM303ids-usidst.xml

us-ids.dtd

us-ids.xsl

Comments

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0 MAY 2 6 2004

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Attorney Docket Number: ATM-303

Art Unit:

2811

Search string:

(5424233 or 5917215 or 6043530 or 6323088 or 20020074583).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5424233	1995-06-13	Yang et al.	A1	437	43
:	2	5917215	1999-06-29	Chuang et al.	A1	257	321
	3	6043530	2000-03-28	Chang	A1	257	.320
	4	6323088	2001-11-27	Gonzalez et al.	A1	438	. 257

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020074583	2002-06-20	Sugiyama et al.	A1	257	298

Signature

Examiner Name	Date		